

ANNUAL

Braemac Technology Conference

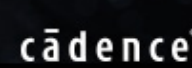
From Electronics Design to Industrial Measurements

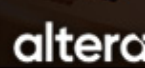
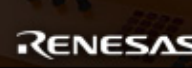


Christchurch/CHCH – Aug 27
Adelaide/ADL – Sep 1
Sydney/SYD – Sep 3

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Schedule	Session Event
08:30 - 09:00	Registration & Coffee Break
09:00 - 09:40	Braemac Welcome - Opening Keynote

SESSION TRACKS

ADELAIDE, AUSTRALIA

Schedule	Design and Simulation
9:40 - 10:20	Cadence Agentic AI for PCB Design — <i>Cadence</i>
10:20 - 10:40	Accelerating Innovation with Altera FPGA — <i>Braemac</i>
11:00 - 11:20	COFFEE BREAK
11:20 - 12:00	Designing for the Extreme: Deploying Mission-Critical Edge Computing in Harsh Environments — <i>ADLINK Technology</i>
12:00 - 13:00	LUNCH AND DEMOS
13:00 - 13:30	Accelerate RF PCB Products to Market — <i>Cadence</i>
13:30 - 14:00	The Display is the Easy Part — <i>Braemac</i>
14:00 - 14:30	MPUs and Edge AI Designs — <i>Renesas Electronics</i>
14:30 - 15:00	CLOSING REMARKS <i>Lucky Draw and Giveaways</i>

Schedule	Test and Measurement
9:40 - 10:20	Meet Nigel AI - The First AI Built for Test — <i>Emerson/NI</i>
10:20 - 10:40	Distributed Lab and Facility Monitoring at Scale — <i>Emerson/NI</i>
10:40 - 11:00	From Wireless Prototyping to RF Test — <i>Braemac</i>
11:00 - 11:20	COFFEE BREAK
11:20 - 12:00	DAQ from Basic Sensors to Rocket Science — <i>Braemac</i>
12:00 - 13:00	LUNCH AND DEMOS
13:00 - 13:30	NI Hardware in Action: Practical T&M Case Studies — <i>Resonate Systems</i>
13:30 - 14:00	Future-Proof Your Test Strategies with NI PXI — <i>Emerson/NI</i>
14:00 - 14:30	Electronics Prototyping from Classroom to Lab — <i>Digilent</i>
14:30 - 15:00	CLOSING REMARKS <i>Lucky Draw and Giveaways</i>